

FIG.4

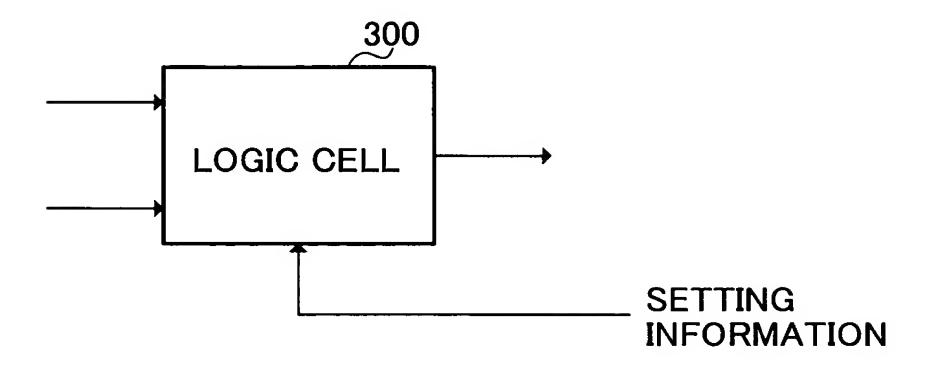
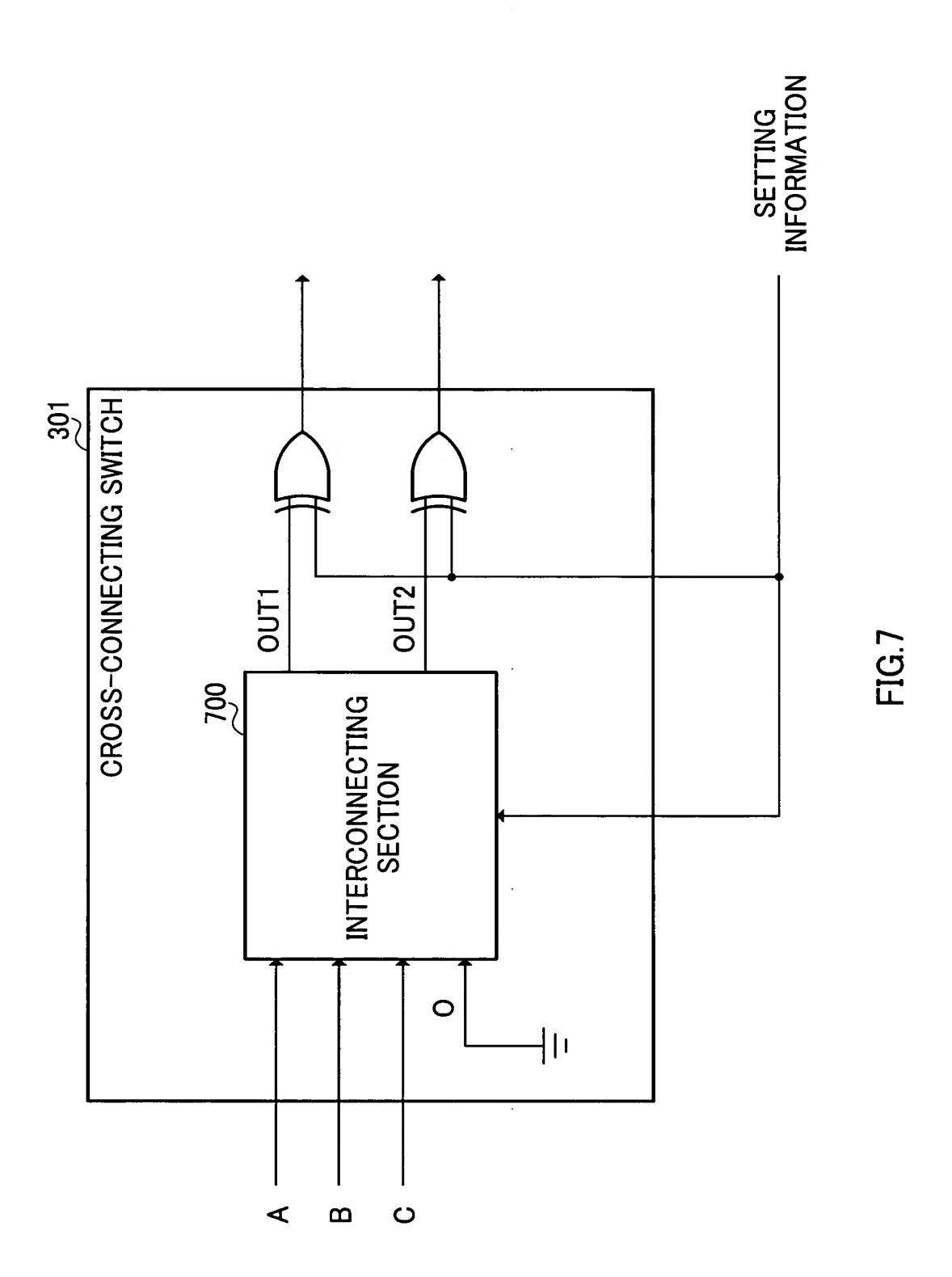


FIG.5

		SETTING INFORMATION			
		00	01	10	11
INPUT DATA	00	0	0	0	1
	01	1	0	1	0
	10	1	0	1	0
	11	1	1	0	0

FIG.6



		OUTPUT DATA		
		OUT1	OUT2	
SETTING INFORMATION	0000	Α	Α	
	0001	Α	В	
	0010	Α	С	
	0011	Α	0	
	0100	В	Α	
	0101	В	В	
	0110	В	С	
	0111	В	0	
	1000	С	Α	
	1001	С	В	
	1010	С	С	
	1011	C	0	
	1100	0	Α	
	1101	0	В	
	1110	0	С	
	1111	. 0	0	

FIG.8

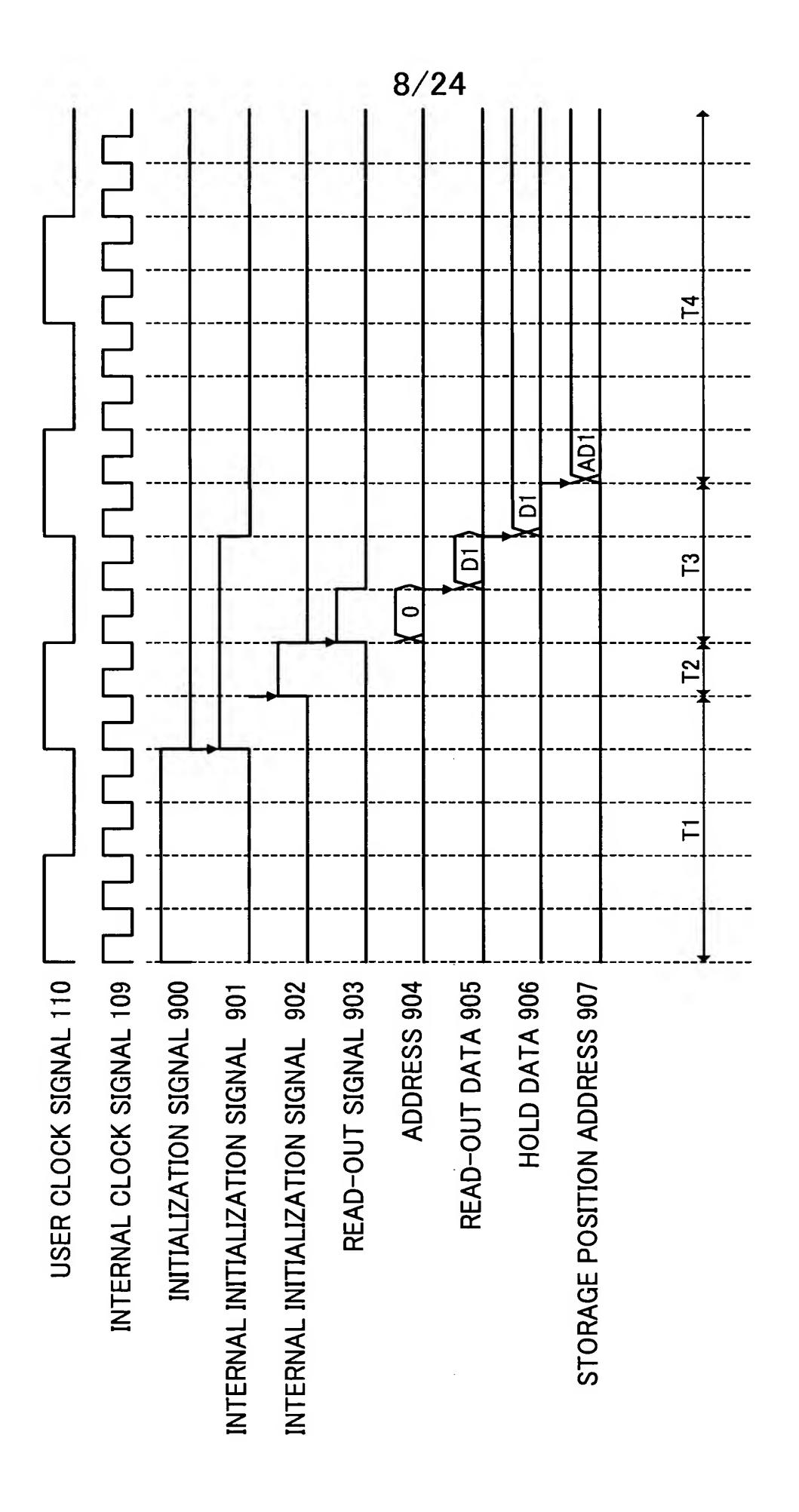


FIG 6

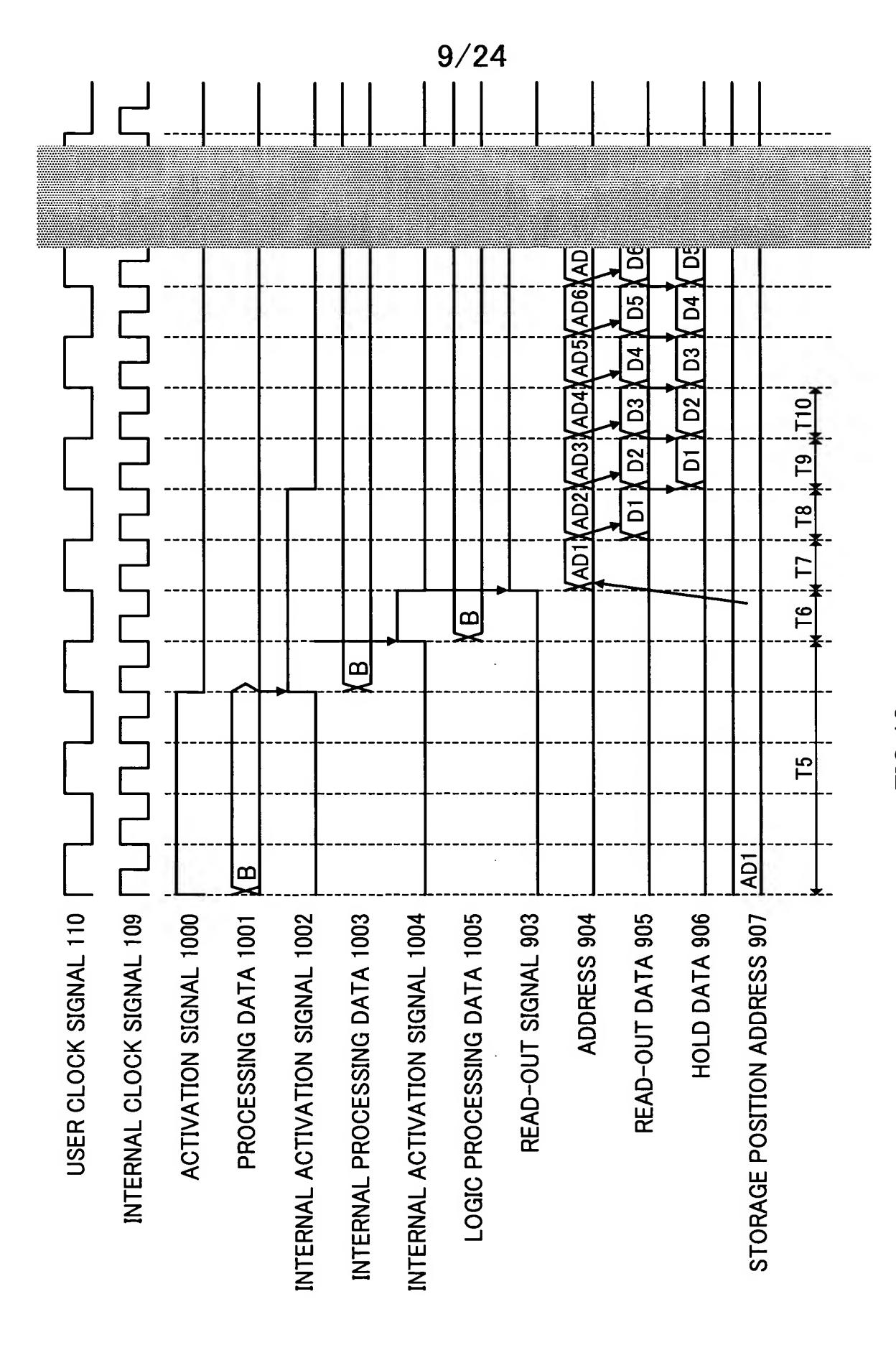
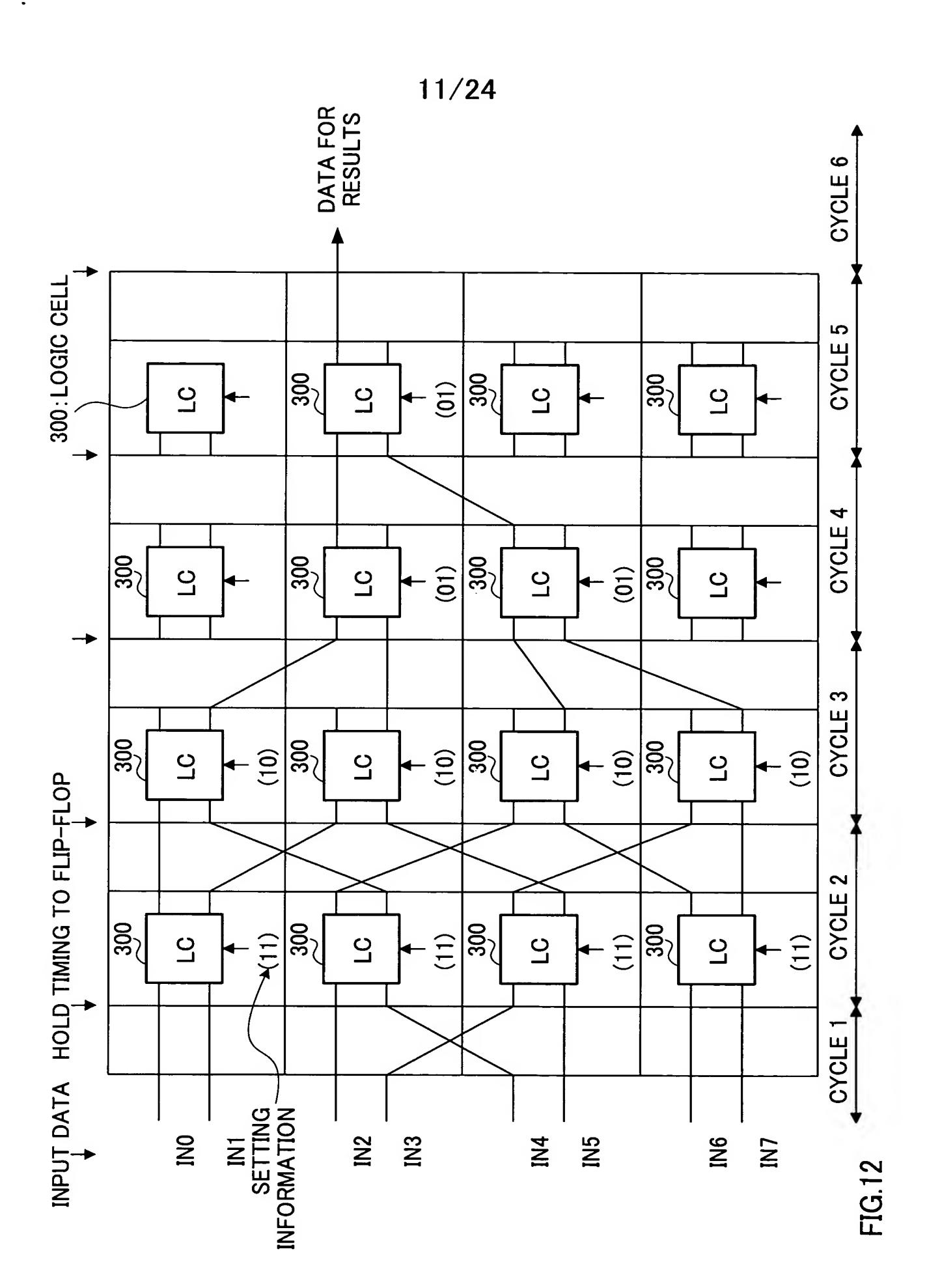
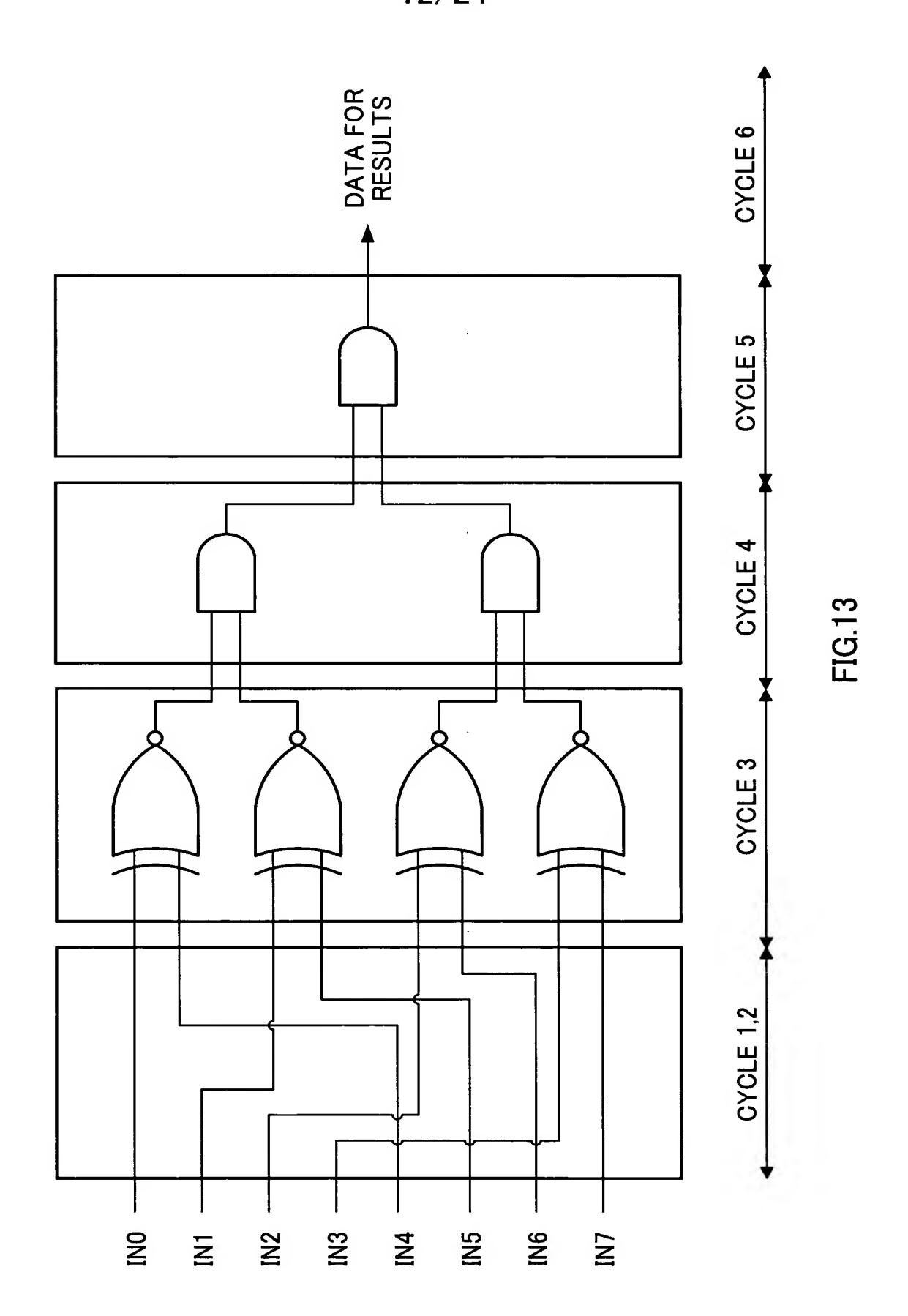


FIG. 10

	SETTING INFORMATION				
		00	01	10	11
INPUT DATA	00	01	01	01	00
	01	10	01	10	01
	10	10	01	10	10
	11	10	10	01	11

FIG.11





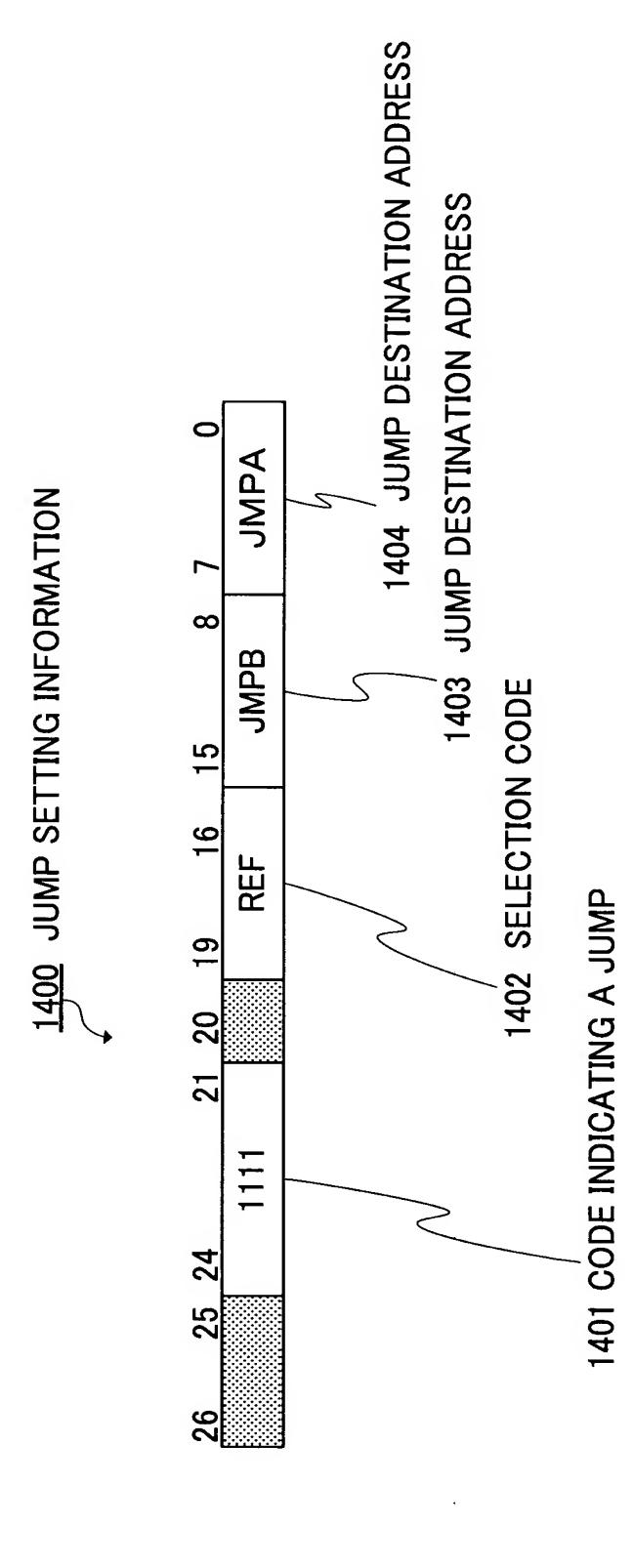


FIG.14

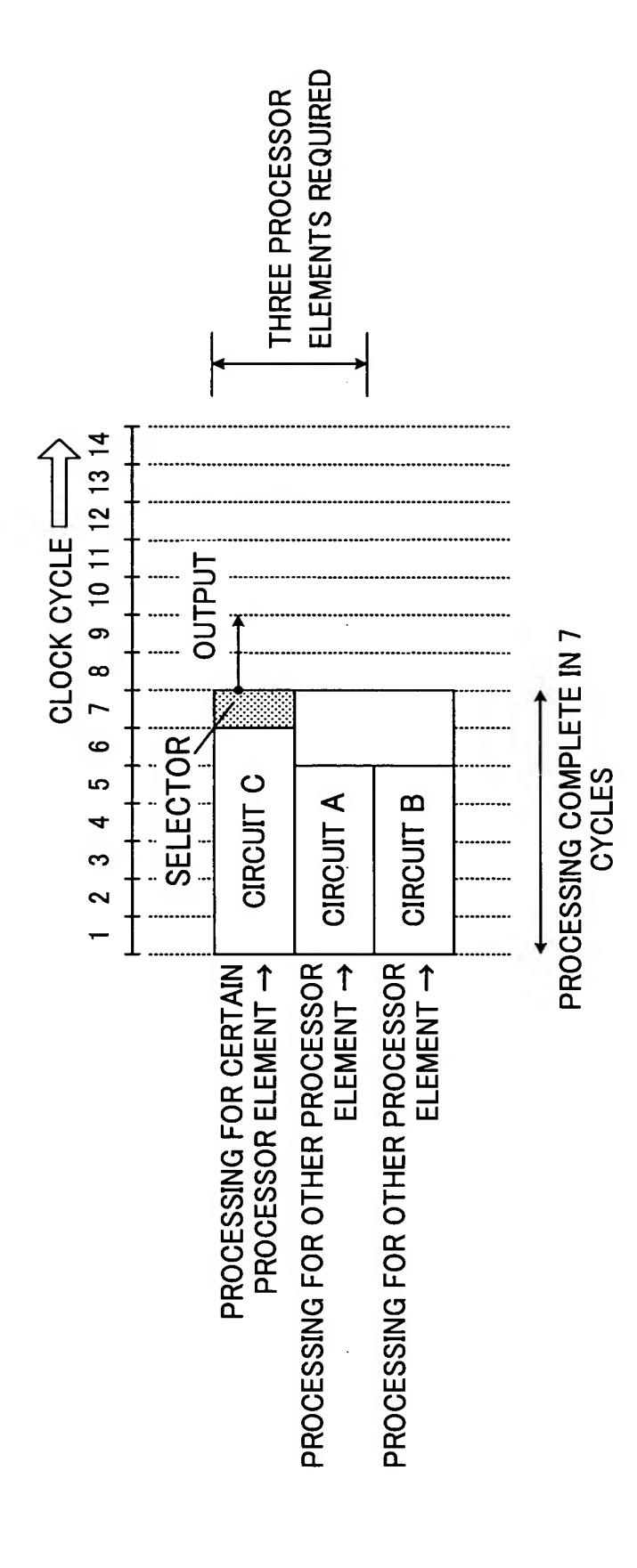


FIG. 16

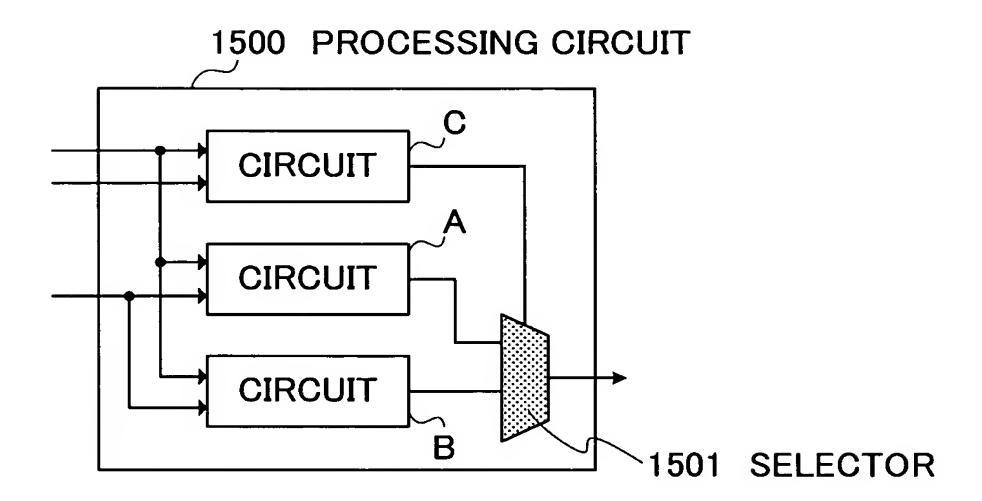


FIG.15

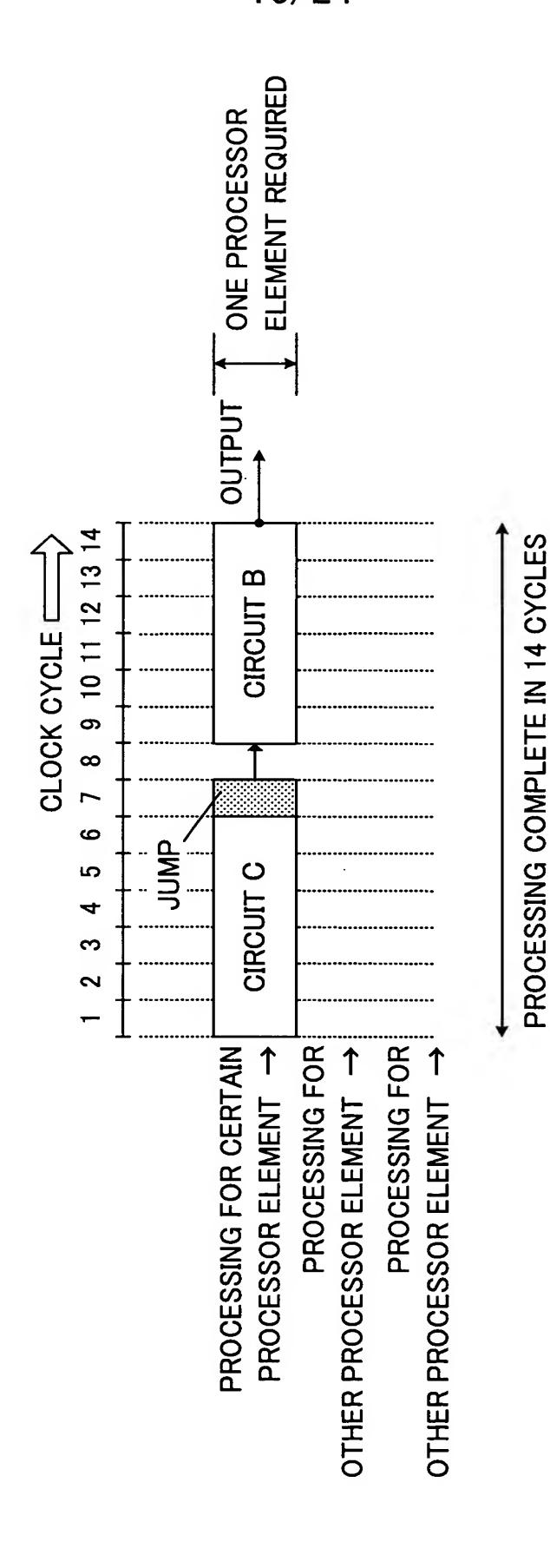


FIG.17

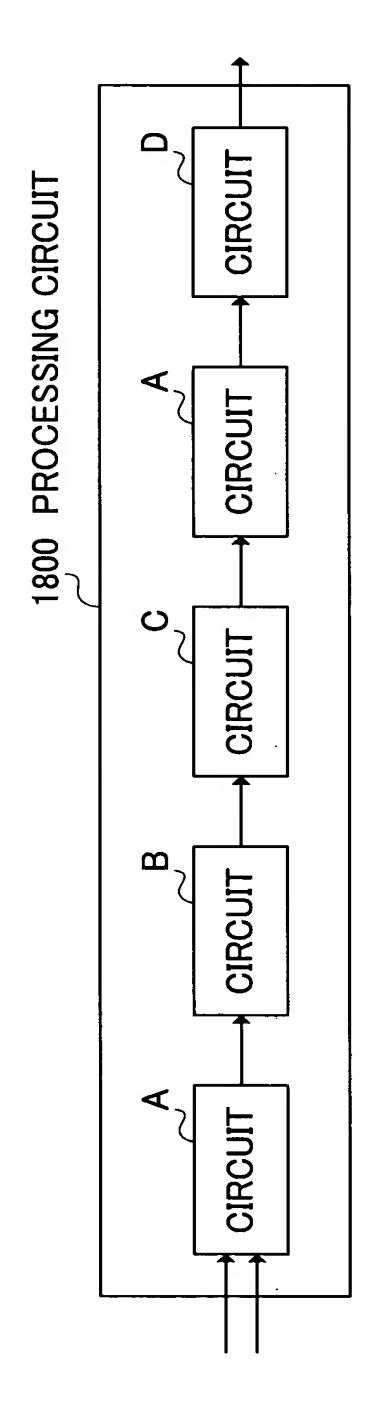


FIG.18

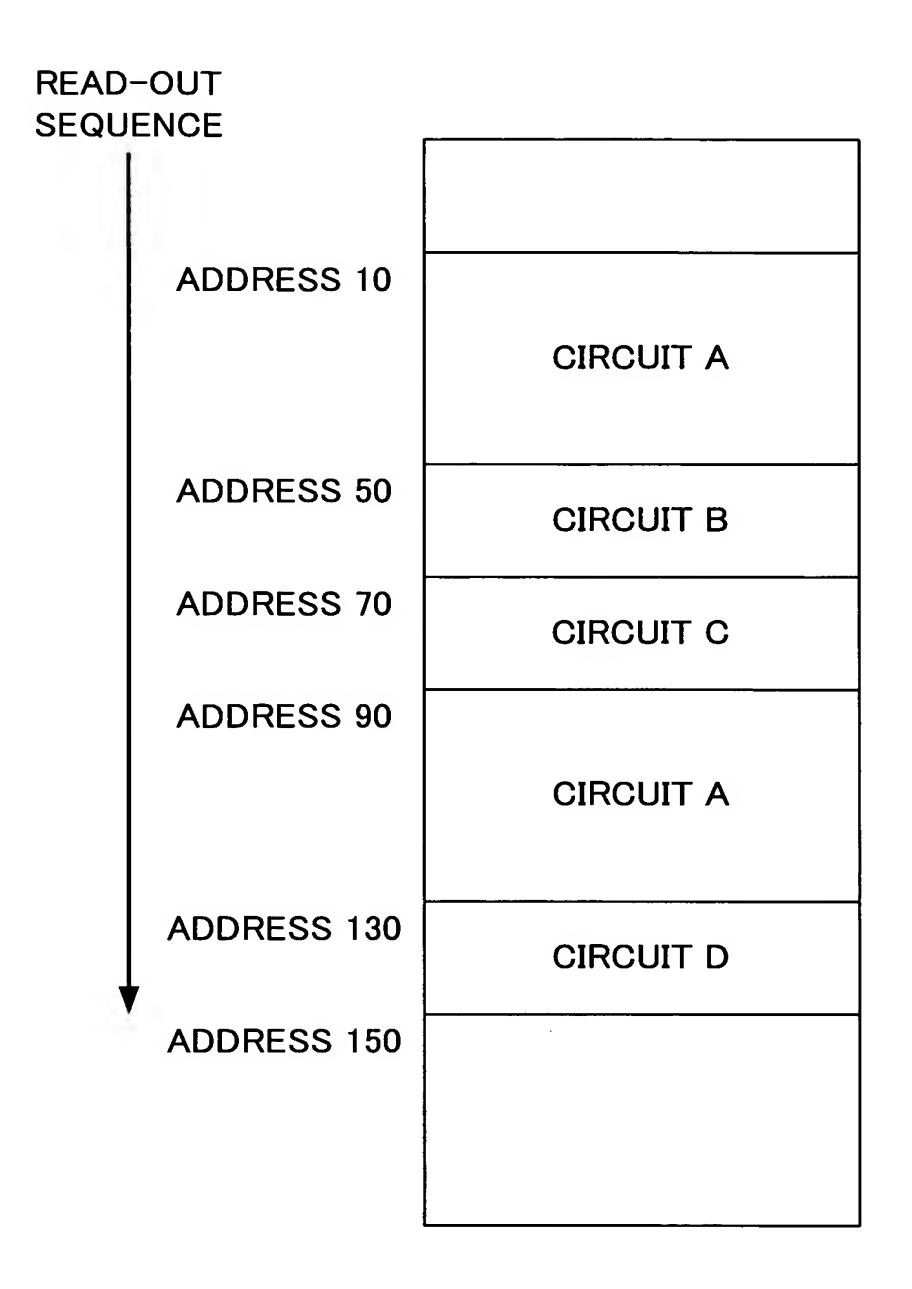


FIG.19

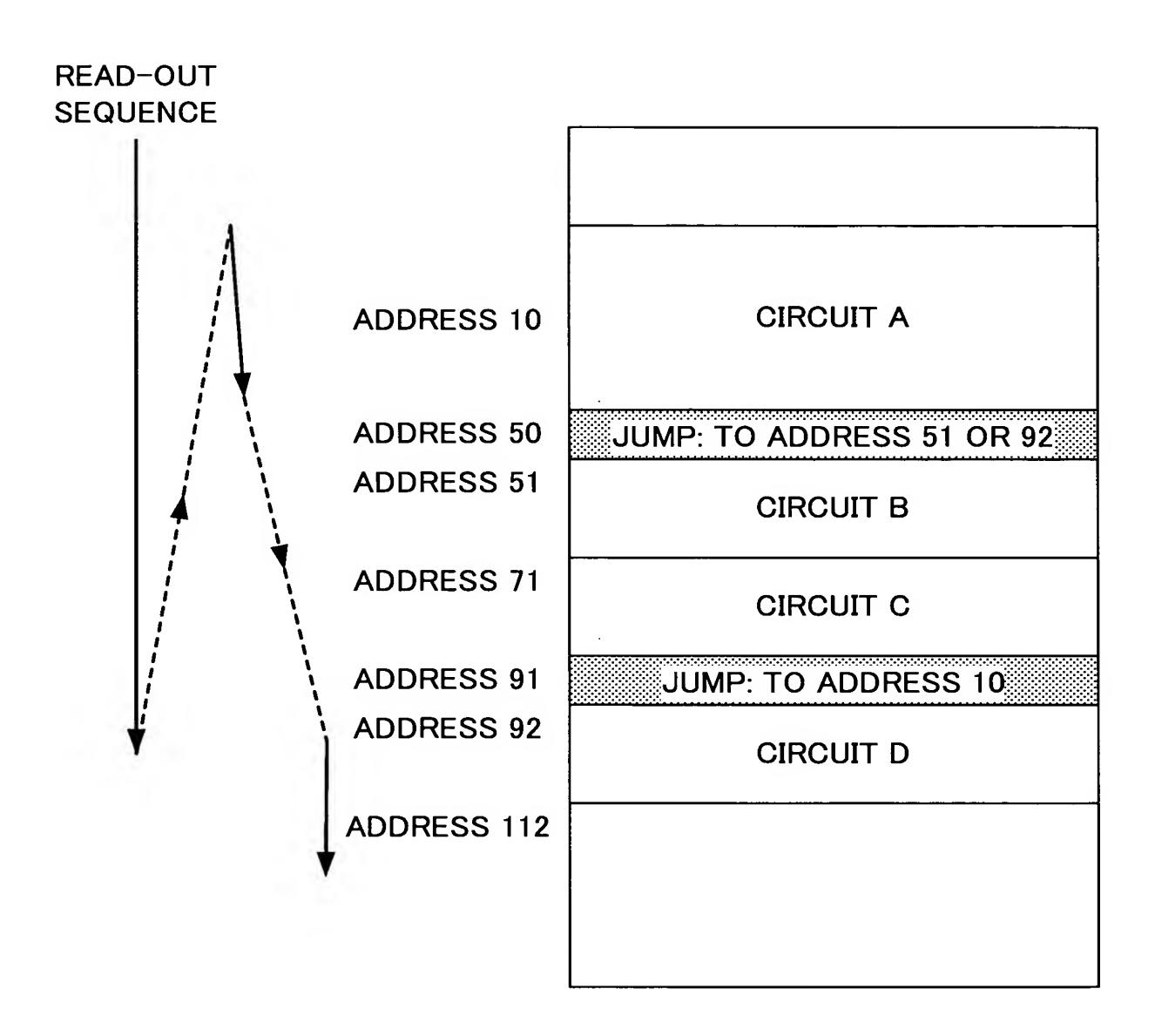


FIG.20

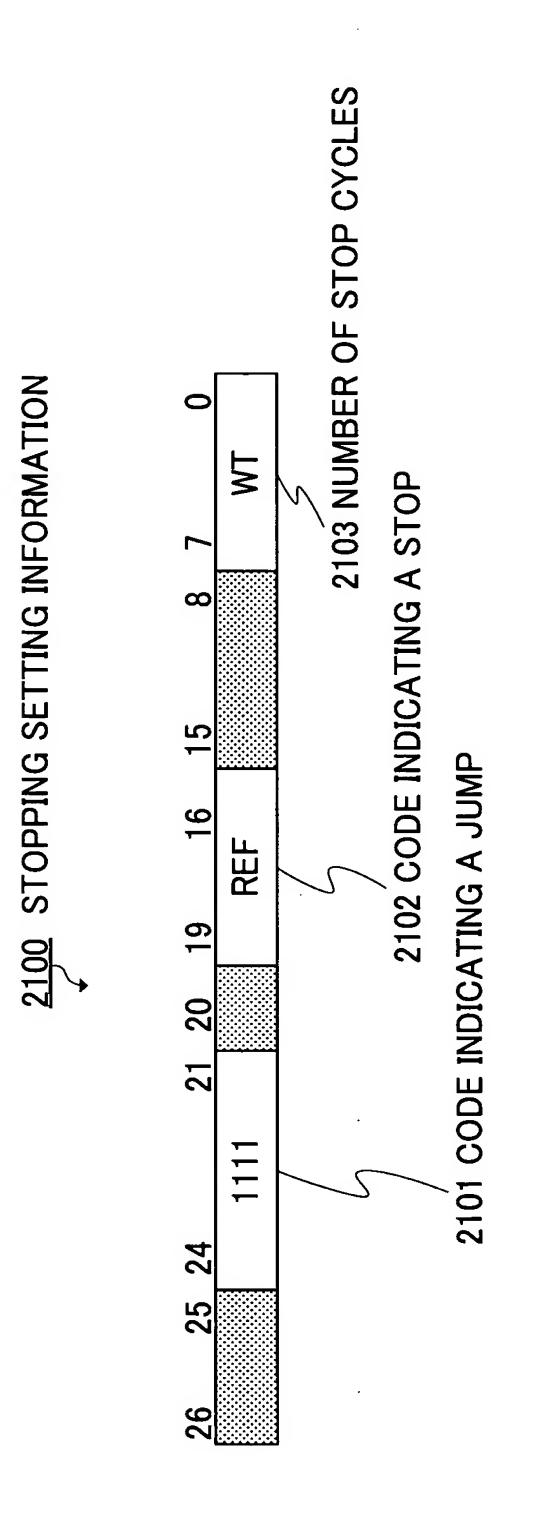
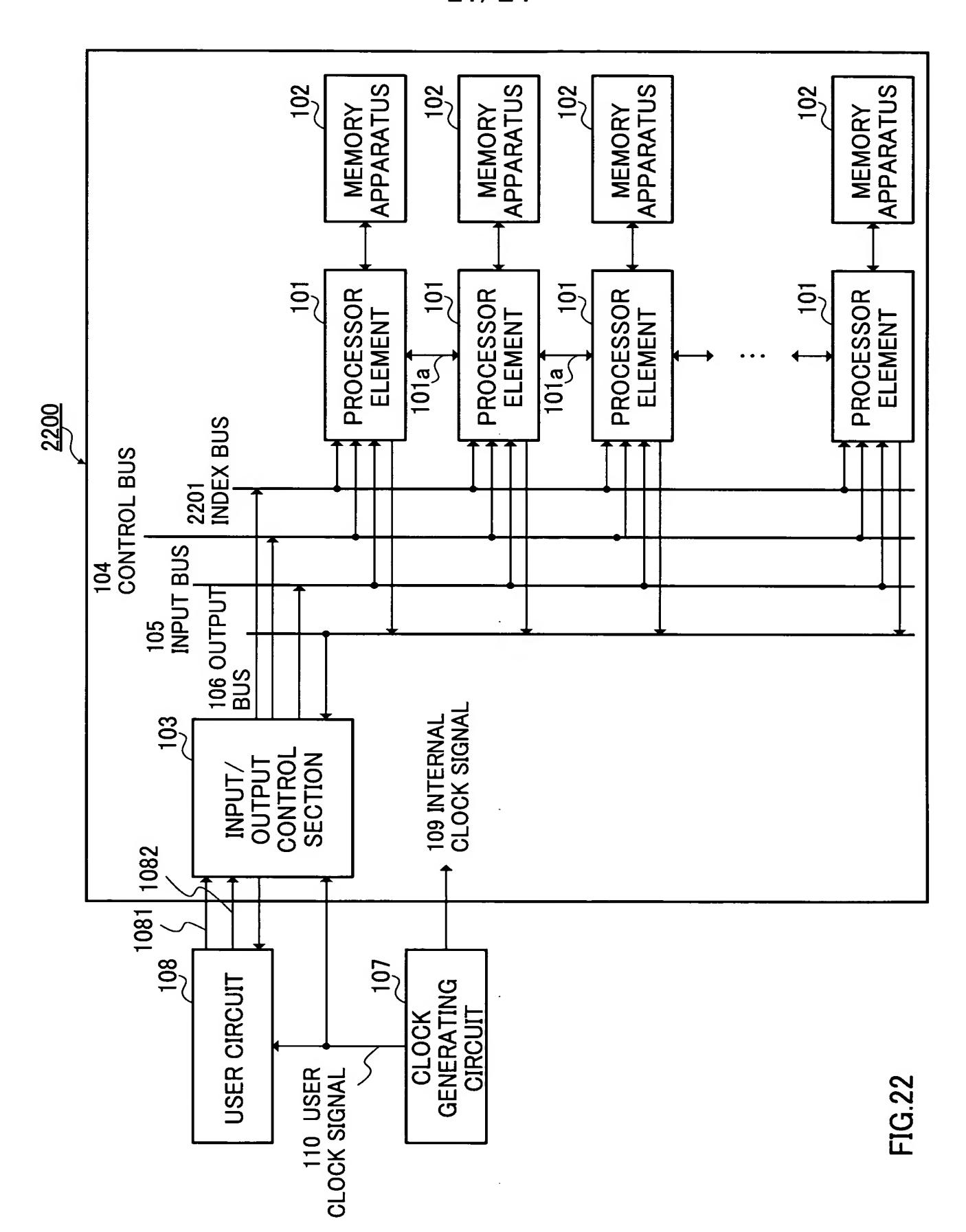


FIG. 2



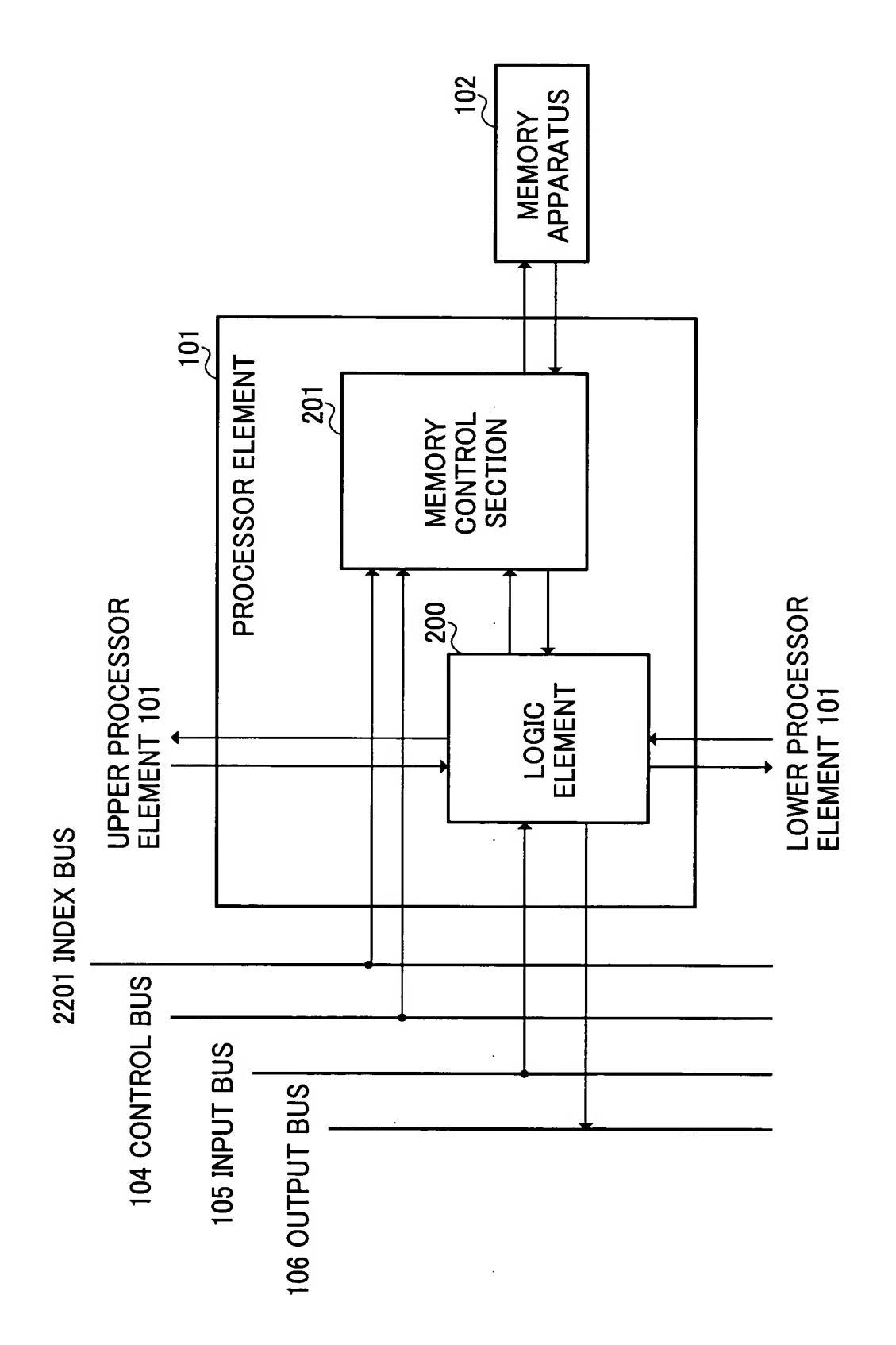


FIG.23

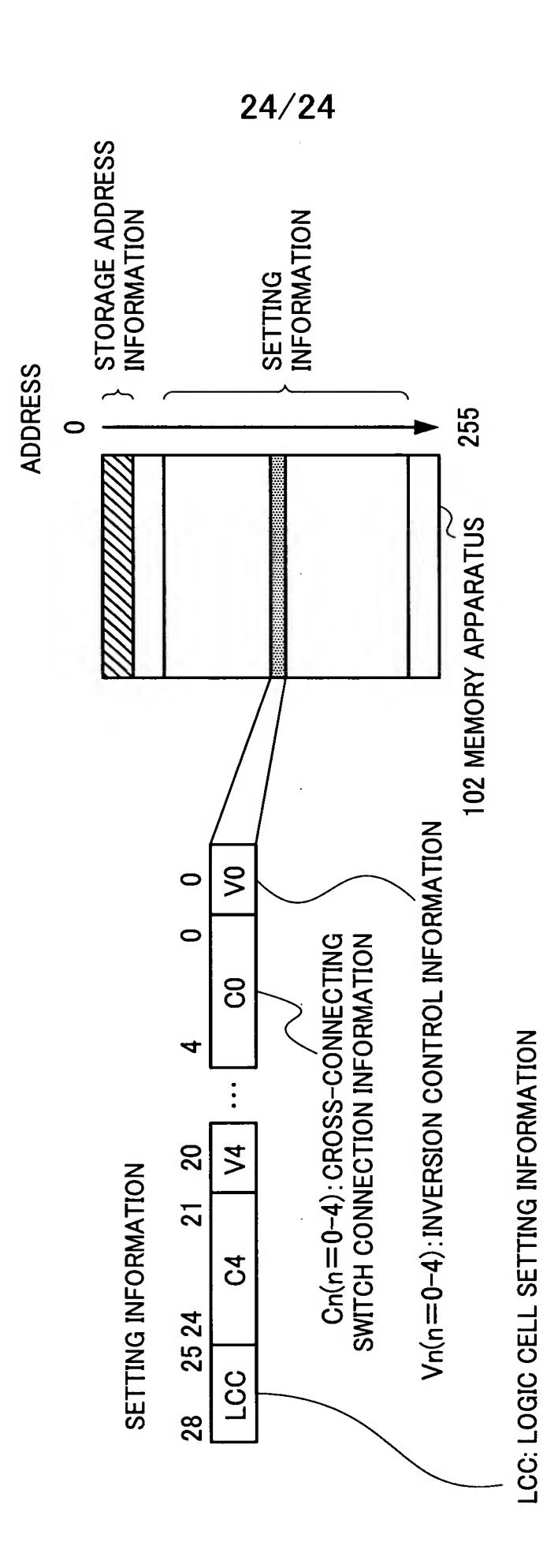


FIG.2